

Aug. 11, 2009



To: Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Fr: Stephen B. Ackerman, Reg. No. 37,761  
28 Davis Avenue  
Poughkeepsie, N.Y. 12603

Subject: Serial No. 09/523,990 Mar. 13, 2000

MOU-SHIUNG LIN

"METHOD OF MANUFACTURE AND IDENTIFICATION OF SEMICONDUCTOR CHIP MARKED FOR IDENTIFICATION WITH INTERNAL MARKING INDICIA AND PROTECTION THEREOF BY NON-BLACK LAYER AND DEVICE PRODUCED THEREBY"

Grp. Art Unit: 2887 WALSH, DANIEL I

#### RESPONSE TO FINAL OFFICE ACTION AND ADVISORY OFFICE ACTION

Dear Sir:

In response to the Final Office Action mailed May 13, 2008 and Advisory Office Action mailed Jul. 27, 2009, please consider the remarks, as follows.

#### CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on Aug. 13, 2009.

Stephen B. Ackerman, Reg. No. 37,761

Signature Stephen B. Ackerman  
Date August 13, 2009